

Docket No.: 056937-0091

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Yukio SUGIMURA, et al.

Application No.: 10/663,993

Filed: September 17, 2003

: Customer Number: 20277

: Confirmation Number: 1975

: Group Art Unit: 2114

: Examiner: David G. Gentry

For: TECHNIQUE FOR TESTABILITY OF SEMICONDUCTOR INTEGRATED CIRCUIT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith is an Amendment in the above-identified application.

☒
☐
☐

No additional fee is required.

Applicant is entitled to small entity status under 37 CFR 1.27

Also attached:

The fee has been calculated as shown below:

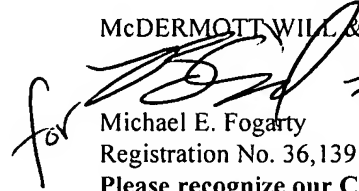
	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	5	20	0	\$50.00 =	\$0.00
Independent Claims	2	3	0	\$200.00 =	\$0.00
Multiple dependent claims newly presented					\$0.00
Fee for extension of time					\$0.00
					\$0.00
Total of Above Calculations					\$0.00

☐ Please charge my Deposit Account No. 500417 in the amount of \$0.00. An additional copy of this transmittal sheet is submitted herewith.

☒ The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

for  Michael E. Fogarty

Registration No. 36,139

Please recognize our Customer No. 20277 as our correspondence address.

600 13th Street, N.W.
Washington, DC 20005-3096
Phone: 202.756.8000 MEF:TT:lnm
Facsimile: 202.756.8087
Date: August 17, 2006
WDC99 1271683-1.056937.0091

Docket No.: 056937-0091



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 20277
Yukio SUGIMURA, et al.	:	Confirmation Number: 1975
Application No.: 10/663,993	:	Group Art Unit: 2114
Filed: September 17, 2003	:	Examiner: David G. Gentry
For: TECHNIQUE FOR TESTABILITY OF SEMICONDUCTOR INTEGRATED CIRCUIT	:	

AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action of May 19, 2006, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 5 of this paper.